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AN8806SB

Three-Beam Method Head Amplifier IC for CD Player

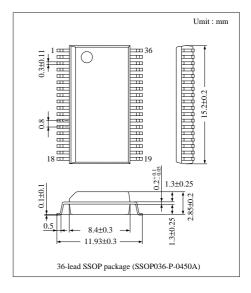
Overview

The AN8806SB is a head amplifier IC supporting the digital servo.

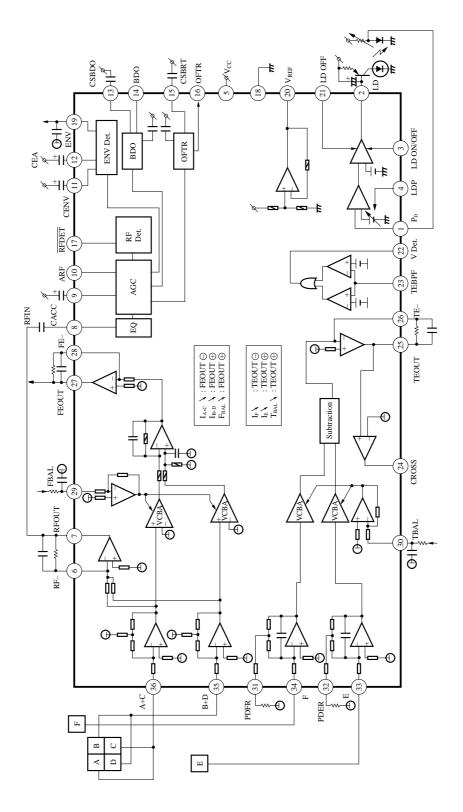
Combination of this IC and the MN6627XX series allows the rational CD system to be constructed with small number of external parts.

Features

- Single supply operation
- I–V amplifier, tracking error amplifier, focus error amplifier, RF amplifier and APC built-in
- Detectors of DO, CROSS, OFTR and vibration built-in
- Combination with the MN6627XX series providing complete automatic adjustment for tracking balance, tracking gain, tracking offset, focus balance, focus gain and focus offset



Block Diagram



Panasonic

Pin No.	Pin Name	I/O	Function Description	
1	P _D	Ι	Signal input of P_D for LD output monitor	
2	LD	0	Connected to base of external Tr for LD drive	
3	LDON	Ι	ON/OFF switching of LD APC	
4	LDP	Ι	Switching of APC reference voltage	
5	V _{CC}		Supply connecting pin	
6	RF-	Ι	Reverse rotation input pin of RF Amp. for connecting a resistance	
7	RFOUT	0	Output pin of RF Amp. (reverse rotation amp.)	
8	RFIN	Ι	RF AGC input pin	
9	C. AGC		C-connecting pin for RF AGC loop filter	
10	ARF	0	RF output pin after AGC	
11	C. ENV		C-connecting pin for RF	
12	C. EA		C-connecting pin for amp.	
13	C. SBDO		C-connecting pin for low speed detection of dark level DO	
14	BDO	0	BDO detection output pin; positive logic	
15	C. SBRT		C-connecting pin for OFTR low speed detection	
16	OFTR	0	OFF TRACK detection output pin; positive logic	
17	NRFDET	0	Output pin of RF signal amplitude detection ; negative logic	
18	GND		Ground pin	
19	ENV	0	ENV output pin	
20	V _{REF}	0	$V_{CC} \times 0.5$ (V) output pin	
21	LD OFF	Ι	Input pin for forced stop of LD APC	
22	VDET	0	Output pin of vibration detection	
23	TEBPF	Ι	Input pin of vibration detection	
24	CROSS	0	Output pin of TE CROSS detection signal	
25	TEOUT	0	Output pin of TE Amp.	
26	TE-	Ι	Inverting input pin of TE Amp. for connecting a resistance	
27	FEOUT	0	Output pin of FE Amp.	
28	FE-	Ι	Inverting input pin of FE Amp. for connecting a resistance	
29	FBAL	Ι	Control signal input pin for FO balance adjustment	
30	TBAL	Ι	Control signal input pin for TE balance adjustment	
31	PDFR		Resistance-connected pin setting the I-V conversion resistance value of PDH	
32	PDER		Resistance-connected pin setting the I–V conversion resistance value of PDF	
33	PDE	Ι	Connected to PIN Diode E	
34	PDF	Ι	Connected to PIN Diode F	
35	PDBD	Ι	Connected to B/D of astigmatic 4-division P _D	
36	PDAC	Ι	Connected to A/C of astigmatic 4-division P _D	

■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{CC}	6	V
Supply Current	I _{CC}	40	mA
Power Dissipation	PD	240	mW
Operating Ambient Temperature	T _{opr}	-30 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125	°C

■ Recommended Operating Range (Ta=25°C)

Parameter	Symbol	Range
Operating Supply Voltage Range	V _{CC}	3.4V ~ 5.5V

■ Electrical Characteristics (Ta=25°C±2°C)

Parameter	Symbol	Condition	min.	typ.	max.	Unit
Total Circuit Current	\mathbf{I}_{tot}	V _{CC} =5V	10.0	14.5	19.0	mA
V _{REF} Output Voltage	V _{REF}	V _{CC} =5V	2.20	2.50	2.80	V
Focus Error Amp.						
Output Offset Voltage of Focus Error Amp.	$V_{\text{FO}-\text{OF}}$	V _{CC} =5V	-250	0	250	mV
Crosstalk Quantity of Focus Error Offset Balance	V _{FO-OFB}	$V_{CC}=5V, V_{FB}=V_{REF}\pm 1V$ Applied	-200		200	mV
I –V Conversion Gain of Focus Error Amp.	\mathbf{R}_{FO}	$V_{CC}=5V, V_{FB}=V_{REF}$	65	93	121	kΩ
I –V Conversion Relative Gain of Focus Error Amp.	DR _{FO}	$V_{CC}=5V, V_{FB}=V_{REF}$	-12	0	12	%
Focus Balance Variable Range 1–H	$B_{\rm FO1-H}$	$V_{CC}=5V, V_{FB}=V_{REF}+1V$	0.15	0.25	0.35	
Focus Balance Variable Range 1–L	$B_{\text{FO1-L}}$	$V_{CC}=5V, V_{FB}=V_{REF}-1V$	1.19	1.70	2.21	
Focus Balance Variable Range 2–H	B_{FO2-H}	$V_{CC}=5V, V_{FB}=V_{REF}+1V$	1.19	1.70	2.21	
Focus Balance Variable Range 2–L	$B_{\text{FO2-L}}$	$V_{CC}=5V, V_{FB}=V_{REF}-1V$	0.15	0.25	0.35	
Frequency Characteristics of Focus Error Amp.	$G_{\rm FO}$	V _{CC} =5V, f=1kHz, 30kHz Input	-4.5	-3	-1.5	dB
Tracking Error Amp.						
Output Offset Voltage of Tracking Error Amp.	V _{TR-OF}	V _{CC} =5V	-63	0	63	mV
Crosstalk Quantity of Tracking Error Offset Balance	V _{TR-OFB}	$V_{CC}=5V, V_{TB}=V_{REF}\pm 1V$ Applied	-200		200	mV
I–V Conversion Gain of Tracking Error Amp.	R _{TR}	V _{CC} =5V, V _{TB} =V _{REF}	420	600	780	kΩ
I–V Conversion Relative Gain of Tracking Error Amp.	DR _{TR}	$V_{CC}=5V, V_{TB}=V_{REF}$	-15	0	15	%
Tracking Balance Variable Range 1–H	B_{TR1-H}	$V_{CC}=5V, V_{TB}=V_{REF}+1V$	0.185	0.285	0.385	
Tracking Balance Variable Range 1–L	B_{TR1-L}	$V_{CC}=5V, V_{TB}=V_{REF}-1V$	1.20	1.75	2.30	
Tracking Balance Variable Range 2–H	B_{TR2-H}	$V_{CC}=5V, V_{TB}=V_{REF}+1V$	1.20	1.75	2.30	
Tracking Balance Variable Range 2–L	B_{TR2-L}	$V_{CC}=5V, V_{TB}=V_{REF}-1V$	0.175	0.275	0.375	
Frequency Characteristics of Tracking Error Amp.	G_{TR}	V _{CC} =5V, f=1kHz, 46kHz	-4.5	-3	-1.5	dB
CROSS Detection						
CROSS Output H Level	V_{CR-H}	V _{CC} =5V, V _{TB} =V _{REF} , f=2kHz	4.2			V
CROSS Output L Level	V _{CR-L}	V _{CC} =5V, V _{TB} =V _{REF} , f=2kHz			0.8	V
RF-Amp.						
RF-Amp. Offset Voltage	$V_{\text{RF-OF}}$	V _{CC} =5V	-75	0	75	mV
RF-Amp. I –V Conversion Gain	R _{RF}	V _{CC} =5V	45.0	56.5	68.0	kΩ
RF-Amp. I –V Conversion Relative Gain 1	DR _{RF}	V _{CC} =5V	-10	0	10	%
RF-Amp. Frequency Characteristics 1 Note)	G _{RF}	V _{CC} =5V, f=500kHz, 3MHz	-2.5	0	2.5	dB

Note) The value may fluctuate by the influence of printed board, socket and etc.

ICs for CD/CD-ROM Player

Parameter	Symbol	Condition	min.	typ.	max.	Unit
AGC						
AGC max. Gain	G _{AGC}	V _{CC} =5V, f=500kHz, Sine Wave	11.0	15.5	20.0	dB
EQ Characteristics	DG _{EQ}	V _{CC} =5V, f=1.5MHz, Sine Wave	- 0.2	1.0	2.0	dB
AGC Operation Gain	G _{OP-AGC}	f=500kHz, V_{IN} =500m V_{P-P}	4	6	9	dB
AGC Compressibility	G _{cm-AGC}	f=500kHz, V_{IN} =100m V_{P-P}	2	6	8	dB
AGC Frequency Characteristics	G _{f-AGC}	f=3MHz, V_{IN} =500m V_{P-P}	-1.5	0	2.5	dB
RFDET						
RFDET Detection Operation	V _{NRF}	f=500kHz	0.067	0.105	0.142	V_{P-P}
RFDET H Level	V _{NRF-H}	f=500kHz	4.2			v
RFDET L Level	V _{NRF-L}	f=500kHz			0.8	V
BDO						
BDO Detection Current	I _{BDO}	V _{CC} =5V	1.0	1.4	1.8	μΑ
BDO H Level	V _{BDO-H}	V _{CC} =5V, f=2kHz Rectangle Wave	4.2			v
BDO L Level	V _{BDO-L}	V _{CC} =5V, f=2kHz Rectangle Wave			0.8	v
OFTR						
OFTR Detection Current	I _{OFTR}	V _{CC} =5V	1.0	1.4	1.8	μΑ
OFTR H Level	V _{OFTR-H}	V _{CC} =5V, f=2kHz Rectangle Wave	4.2			V
OFTR L Level	V _{OFTR-L}	V _{CC} =5V, f=2kHz Rectangle Wave			0.8	V
LD – APC						
LD ON H Input Level	V _{LDON-H}	V _{CC} =5V	3.5			V
LD OFF H Input Level	V _{LDOFF-H}	V _{CC} =5V	3.5			V
LD ON Operation Voltage (1)	V _{LD (1)}	V _{CC} =5V, V ₁ : DC Sweep	0.135	0.170	0.205	V
LD ON Operation Voltage (2)	V _{LD (2)}	V _{CC} =5V, V ₁ : DC Sweep	0.116	0.140	0.164	V
V. Detection	1		1			
V. Det. Level (1)	V _{Det. (1)}	V _{CC} =5V, V ₂₃ : DC Sweep	52	75	98	mV
V. Det. Level (2)	V _{Det. (2)}	V _{CC} =5V, V ₂₃ : DC Sweep	-98	-75	-52	mV
V. Det. H Level	V _{DetH}	V _{CC} =5V, V ₂₃ : DC Sweep	4.2			V
V. Det. L Level	V _{DetL}	V _{CC} =5V, V ₂₃ : DC Sweep	—		0.8	V
3TENV						
CENV Detection Current	I _{CENV}	V _{CC} =5V	8.5	11	13.5	μΑ
CEA-ENV Transmission Characteristics	G _{CEA-ENV}	V _{CC} =5V	21	23	25	dB
CEA Input Impedance	R _{CEA}	V _{CC} =5V	6.5	8.25	9.5	kΩ
ENV Output Impedance	R _{ENV}	V _{CC} =5V	6.5	8.25	9.5	kΩ
ENV-Amp. Offset Voltage	V _{OP-ENV}	V _{CC} =5V	-70	0	70	mV
CEA-Amp. Operation Voltage	V _{OP-CEA}	V _{CC} =5V	50	125	200	mV

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• Operational Description (The values shown below are design values when supply voltage $V_{CC} = 5V$.)

RFAmplifier

As shown in Figure 1, RF signals are generated by three-beam method pick-up. Equivalent resistance to RFOUT (Pin7) is $57.6k\Omega$, when resistance between RF–and RFOUT is $12k\Omega$.

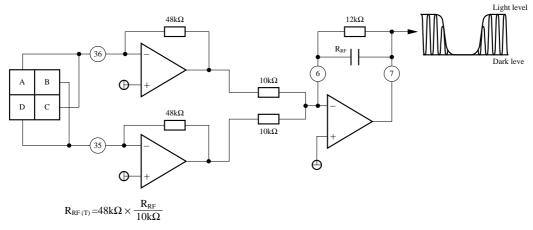


Figure 1 : RF Amplifier

• Focus Error Amplifier

As shown in Figure 2, the operation result of (A + C) - (B + D) is outputted to FEOUT (Pin27). Equivalent resistance to FEOUT is 96k Ω , when resistance between FE and FEOUT is 20k Ω .

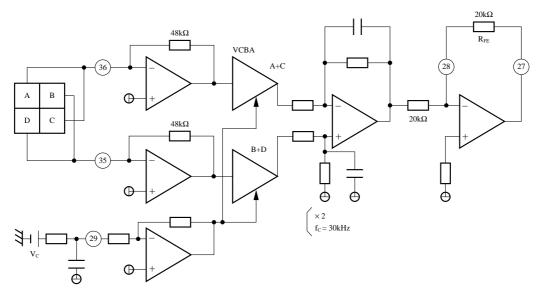


Figure 2 : Focus Error Amplifier

The VCBA is for focus balance adjustment. Connecting 47 k Ω of external resistance to Pin29, signal ratio between A + C and B + D can be controlled as shown in Table 1.

Table 1						
Vc	$\frac{A+C}{B+D}$	$\frac{B+D}{A+C}$				
1.5V	0.14	7.0				
2.5V	1.0	1.0				
3.5V	7.0	0.14				

• Tracking Error Amplifier

As shown in Figure 3, the tracking error amplifier receives the output currents E and F of three-beam method pin photo diode for sub-beam detection, and outputs the operation result of E - F to TEOUT (Pin25).

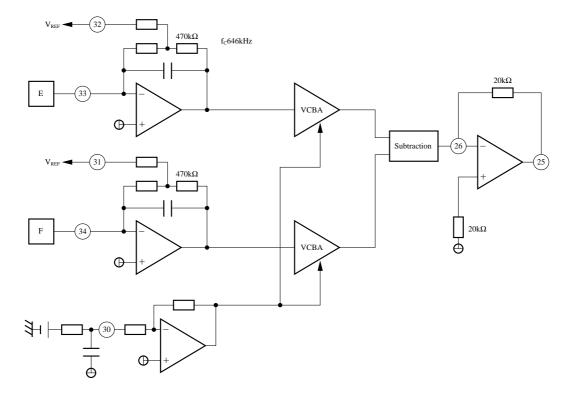


Figure 3 : Tracking Error Amplifier

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Pins31 and 32 are used to make the resistance converting the current to voltage variable. When these pins are directly connected toV_{REF} and $20k\Omega$ of resistance is connected between TE and TE–, and between TE and TEOUT respectively, equivalent resistance to TEOUT is $629k\Omega$.

	Table 2	
Vc	E/F	F/E
1.5V	0.14	7.0
2.5V	1.0	1.0
3.5V	7.0	0.14

The VCBA is for tracking balance adjustment in the same way of for focus. Table 2 shows the tracking balance ratio under the same conditions as shown in Table 1.

• AGC

In order to absorb the attenuation of RF signal by disk, pick-up dispersion, finger print and other factors, AGC circuit for RF is incorporated.

The input impedance of Pin8 is approx. 27.9k Ω . By Pin10 (ARF), the gain-controlled RF-signal is outputted. Input allowable level is approx. $1V_{P-P} \sim 500mV_{P-P} \sim 250mV_{P-P}$, and output is $1V_{P-P}$. Pin9 is a connecting pin with capacity for constructing the AGC loop filter, and it is recommended that the external capacitor of 1µF around should be connected to it for V_{CC}.

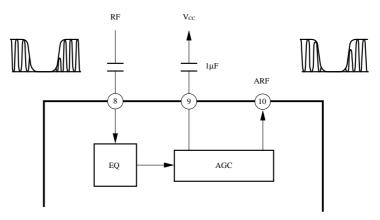


Figure 4 : AGC

• RFDET

The RFDET detects amplitude of RF signal inputted to Pin8. Threshold value is approx. 100mV (f=500kHz), and RF Present=L, RF Absent=H are outputted.

BDO Detection

BDO is detected by the low speed detection circuit and high speed detection circuit. The high speed detection circuit is incorporated. On the other hand, the capacitor of the low speed detection circuit must be connected to Pin13. The capacity value is 2200pF and it is recommended that it should be connected for V_{CC} . When this capacity value of Pin13 is increased, BDO is detected with large RF missing rate. When it is decreased, BDO can be detected with small RF missing rate.

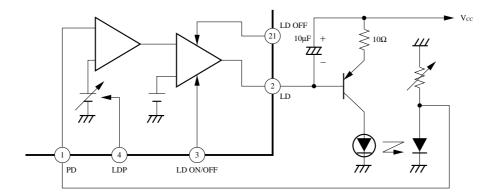
BDO present = H BDO absent = L

• OFTR Detection

OFTR detection reversely corresponds with BDO detection. However, its detection method is the same as for BDO. Capacitor for the low speed detection must be connected to Pin15. It is recommended that it should be connected to V_{CC} at 2200pF in the same way as for Pin13. The relation between the capacity value and OFTR detection timing is as the same for BDO.

OFTR = H Off from OFTR = L

• APC





By connecting Pin4 to OPEN or GND, the reference voltage of LD can be switched. Connection to OPEN makes the reference voltage approx. 170mV and connection to GND makes it approx. 140mV.

Reference Supply

Servo reference supply is incorporated. Pin20 outputs $V_{CC}/2$, and has approx. 3mA of current capacity for sink/source. It is recommended that it should be given 1µF to 10µF of capacity in addition.

Vibration Detection

The AN8806SB has a wind comparator of approx. ±75mV. Abnormal vibration components of tracking error are detected and the data for whether vibration is present or not is outputted to Pin22.

• Envelope Detection

The envelope detection circuit extracts fluctuation of RF signal required for adjustment of focus balance.

Pin11 and 12 are connecting pins with capacity for constructing the filter.

It is recommended that for Pin11, a capacitor of 100 pF around, and for Pin12 , an external capacitor of 0.027μ F around should be connected against V_{CC}.

Pin19 is for output and must have external capacity for constructing the filter, and it is recommended that an external capacitor of 0.027μ F around should be connected to it for V_{REF}.

